

The manufacturer may use the mark:



Revision 1.2 May 24, 2020 Surveillance Audit Due August 1, 2022

Certificate / Certificat

Zertifikat / 合格証

GEO 1704048 C001

exida hereby confirms that the:

P2002 Trip Manifold Assembly (TMA)

Nexus Controls LLC Longmont, CO - USA

Has been assessed per the relevant requirements of:

IEC 61508: 2010 Parts 1-7

and meets requirements providing a level of integrity to:

Systematic Capability: SC 3 (SIL 3 Capable)





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Certificate / Certificat / Zertifikat / 合格証

GEO 1704048 C001

Systematic Capability: SC 3 (SIL 3 Capable)

GEO 1704048 C001								
Systematic Capability: SC 3 (SIL 3 Capable)								
Random Capability: Type A, Route 2 _H Device								
PEH/PED and Architecture Constraints								
PFH/PFD _{avg} and Architecture Constraints Z must be verified for each application \cong								
Systematic Capability: The product has met manufacturer design process requirements of Safety Integrity Level (SIL) 3. These are intended to achieve sufficient integrity against systematic errors of design by the manufacturer. A Safety Instrumented Function (SIF) designed with this product must not be used at a SIL level higher than stated.								
Systematic Capability: SC 3 (SIL 3 Capable) Random Capability: Type A, Route 2 _H Device PFH/PFD _{avg} and Architecture Constraints must be verified for each application Systematic Capability: The product has met manufacturer design process requirements of Safety Integrity Level (SIL) 3. These are intended to achieve sufficient integrity against systematic errors of design by the manufacturer. A Safety Instrumented Function (SIF) designed with this product must not be used at a SIL level higher than stated. Random Capability: The SIL limit imposed by the Architectural Constraints must be met for each element. This device meets exida criteria for Route 2 _H . IEC 61508 Failure Rates in FIT* Table 1 Failure rates with No Online Testing								
Component	λ _{SD}	λ _{su} ¹	$\lambda_{ extsf{DD}}$	λ _{DU} <u>u</u>				
Electronic Trip Device				'				
Solenoid	0	377	0	122				
Cartridge Valve	0	84	0	31				
Table 2 Failure rates with Online Testing								
Component	λ_{SD}	λ _{su}	λ_{DD}	λου				
Electronic Trip Device				12				
Solenoid	377	0	110	12				
Cartridge Valve	0	84	0	31				
* FIT = 1 failure / 10° hours † PVST = Partial Valve Stroke Test of a final element Device **SIL Verification: The Safety Integrity Level (SIL) of an entire Safety Instrumented Function (SIF) must be verified via a calculation of PFH/PFD _{avg} considering redundant architectures, proof test interval, proof test effectiveness, any automatic diagnostics, average repair time and the specific failure rates of all products included in the SIF. Each element must be checked to assure compliance with minimum hardware fault tolerance (HFT) requirements. The following documents are a mandatory part of certification: Assessment Report: GEO 17/04-048 R002 V1 R2 (or later) Safety Manual: GECS10058 Page 2 of 2 CDA/138M9335/000/01								
Safety Manual: GECS10058								
CDA/138M9335/000/01			Page	2 of 2				

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P2002 Trip Manifold

Assembly (TMA)

80 N Main St Sellersville, PA 18960

T-061, V3R2